

SC10098C RR 02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Date 7/11/2005
IN RE APPLICATION OF HALL, JEFFERSON W. et al.
APPLN. NO. 09/709893
FILED 11/13/2000
FOR POWER CONVERSION INTEGRATED CIRCUIT AND
METHOD FOR PROGRAMMING
EXAMINER Rajnikant B. Patel
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JUL 11 2005

CERTIFICATE OF TRANSMITTAL VIA FACSIMILE

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING
FACSIMILE TRANSMITTED TO THE PATENT AND TRADEMARK OFFICE,
ATTN EXAMINER Patel
AT THE FAX No. (703) 872-9306

By


Lydia McNamara

on: July 11, 2005
Date

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

S I R

Applicant(s) respectfully inquire as to the status of the above-identified patent application. The last communication was

Applicants Amendment with surrender of original letters patent dated July 16, 2004; disclosure statement dated 15 December 2004.

Applicants previously inquired into the status of this application on January 24, 2005. No response has been received from the Patent Office.

Please advise us of the current status of this application.

Respectfully submitted,
Jefferson W. Hall et al.



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